

ARM® Cortex®-M0
32-bit Microcontroller

NuMicro® Family
NUC1311 Series BSP
Revision History

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Revision 3.00.005 (Released 2026-07-07)

1. Fixed SPI_Loopback sample code run code issue.
2. Updated ISP_SPI sample to add interrupt mask protection for ParseCmd.
3. Added CLK_PLLCON setting definitions for different HXT frequency.
4. Added read/write byte related functions to the I2C driver.
5. Fixed CAN_CLR_INT_PENDING_BIT in the CAN driver to ensure CAN_IF_CMASK_WRRD matches the current write/read mode.
6. Updated the CAN driver.
7. Added UART_LIN_TxRx sample code.
8. Added a macro to wait for the I2C bus status to be cleared.
9. Added the macro to the sample code to avoid re-entering the interrupt before the register write is complete, which could lead to an incorrect I2C_STATUS0 value of 0xf8.
10. Supported VSCode multi-toolchain projects.
11. Added Schmitt trigger configuration for I2C pins.
12. Fixed infinite loop.

Revision 3.00.004 (Released 2024-06-17)

1. Added CAN_BasicMode_SilentMode and CAN_NormalMode_SilentMode sample code.
2. Used "volatile" with the function pointer to disable compiler optimizations in I2C sample code.
3. Enabled LVR in SYS_PowerDown_MinCurrent sampl code to prevent power on/off fail.
4. Added SYS_PowerDown_MinCurrent sample code.
5. Modified UART_Read() to return received data count when time-out.
6. Modified SYS_UnLockReg() time-out handler.
7. Added timeout handler for infinite loop.
8. Added I2C hang up & recover mechanism for I2C Master and Slave sample code.

Revision 3.00.003 (Released 2021-01-21)

1. Fixed SetMultiRxMsg MsgCount in Library/StdDriver/src/can.c
2. Fixed warnings of adc driver in Library/StdDriver/src/adc.c
3. Added Apache-2.0 license declaration in driver source.
4. Added README.md file.

Revision 3.00.002 (Released 2019-11-12)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.

Revision 3.00.001 (Released 2014-06-12)

1. First Release

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